Description

JMT Dual N-channel Enhancement Mode Power MOSFET

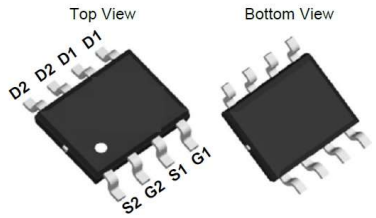
Features

- 20V, 6A
  $R_{DS(ON)}$< 28mΩ @ V_{GS}=4.5V
  $R_{DS(ON)}$< 38mΩ @ V_{GS}=2.5V
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

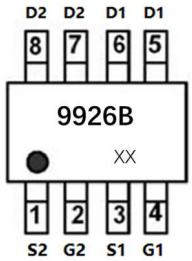
Application

- Load Switch
- PWM Application
- Power management

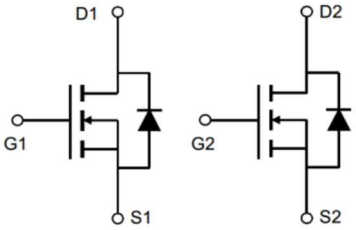
RoHS



SOP-8(Dual)



Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
9926B	JMTP9926B	TAPING	SOP-8	13inch	4000	-

Absolute Maximum Ratings (T_A=25℃ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		20	V
V_{GSS}	Gate-Source Voltage		±12	V
I_D	Continuous Drain Current	T_A= 25℃	6	A
		T_A= 100℃	4	A
I_{DM}	Pulsed Drain Current note1		24	A
P_D	Power Dissipation	T_A= 25℃	1.6	W
$R_{\theta JA}$	Thermal Resistance, Junction to Case		78	℃/W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	℃

Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250μA	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=20V, V_{GS}=0V,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS}=0V, V_{GS} =±12V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250μA	0.4	0.7	1.0	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note2	V_{GS}=4.5V, I_D=6A	-	20	28	mΩ
		V_{GS}=2.5V, I_D=5A	-	25.5	38	
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS}=10V, V_{GS}=0V, f=1.0MHz	-	358	-	pF
C_{oss}	Output Capacitance		-	69.3	-	pF
C_{rss}	Reverse Transfer Capacitance		-	58.5	-	pF
Q_g	Total Gate Charge	V_{DS}=10V, I_D=3A, V_{GS}=4.5V	-	5.6	-	nC
Q_{gs}	Gate-Source Charge		-	0.8	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	1	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=10V, I_D=6A, R_{GEN}=3Ω, V_{GS}=4.5V	-	16	-	ns
t_r	Turn-on Rise Time		-	51	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	21	-	ns
t_f	Turn-off Fall Time		-	19	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	6	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	24	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS}=0V, I_S=6A	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Performance Characteristics

Figure1: Output Characteristics

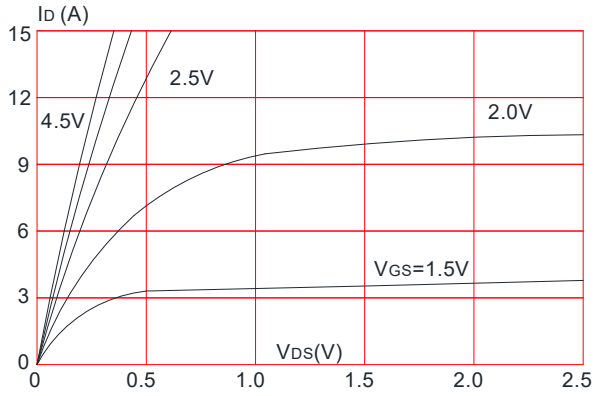


Figure 2: Typical Transfer Characteristics

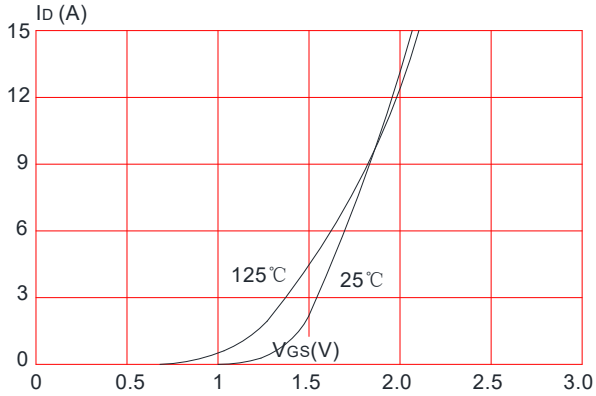


Figure 3: On-resistance vs. Drain Current

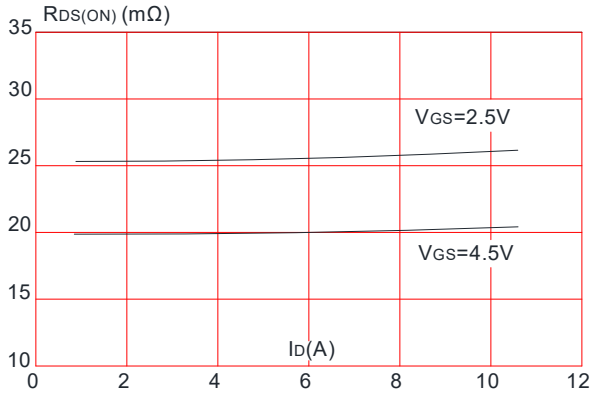


Figure 4: Body Diode Characteristics

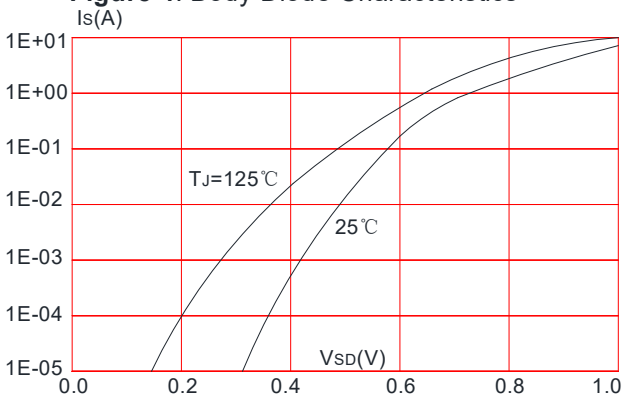


Figure 5: Gate Charge Characteristics

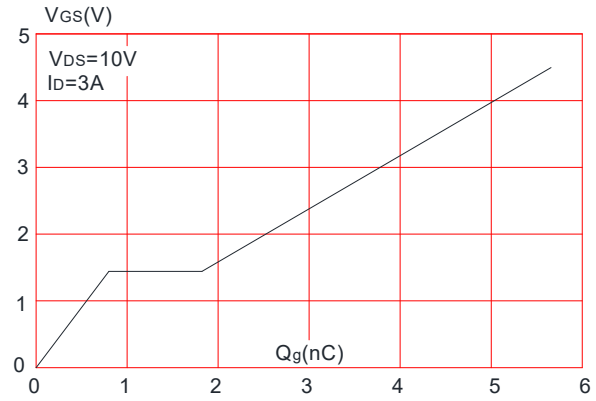


Figure 6: Capacitance Characteristics

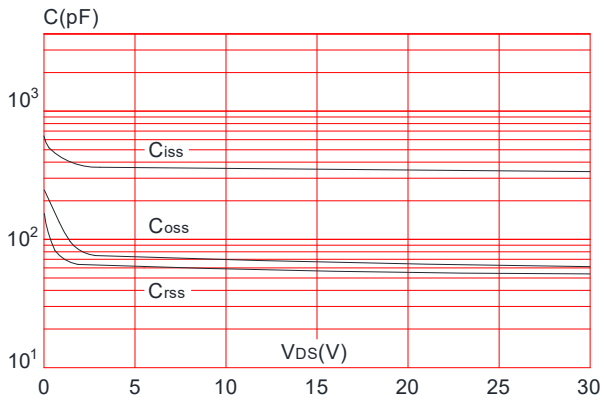


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

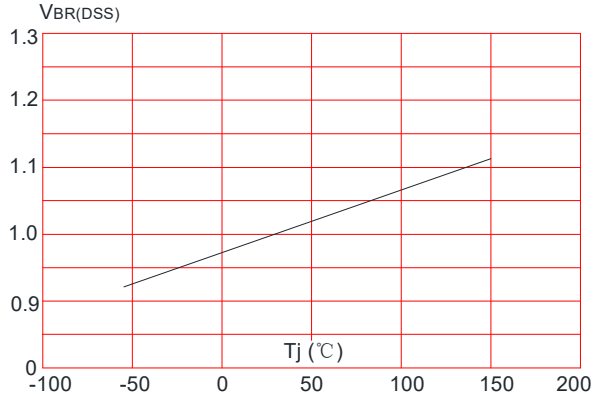


Figure 8: Normalized on Resistance vs. Junction Temperature

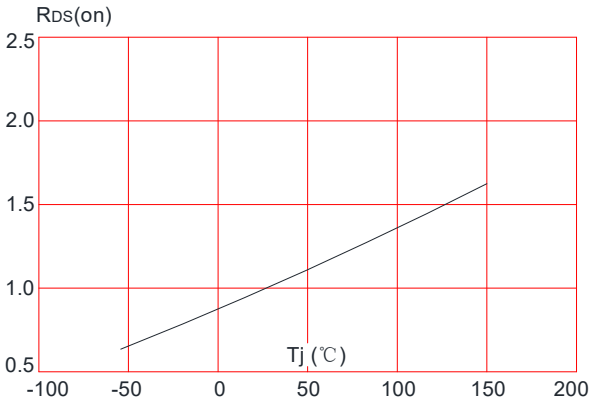


Figure 9: Maximum Safe Operating Area

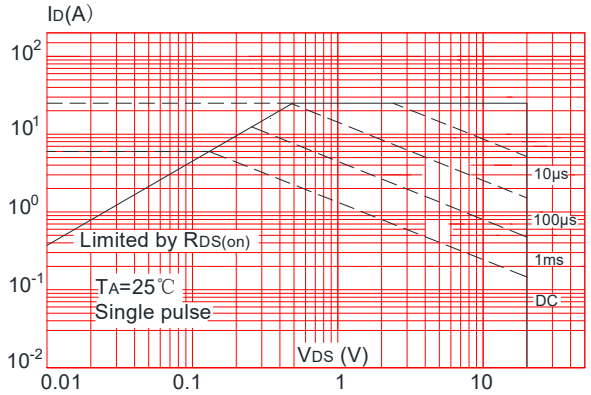


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

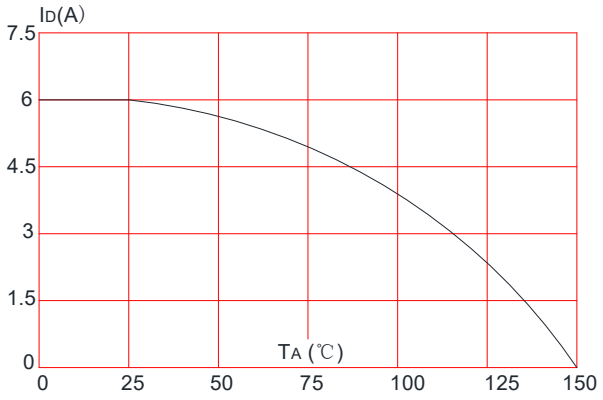
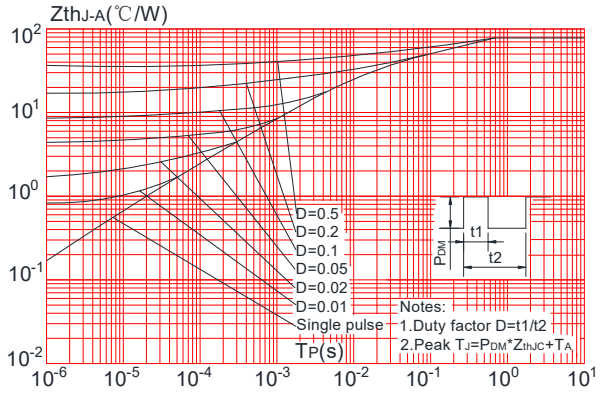


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Test Circuit

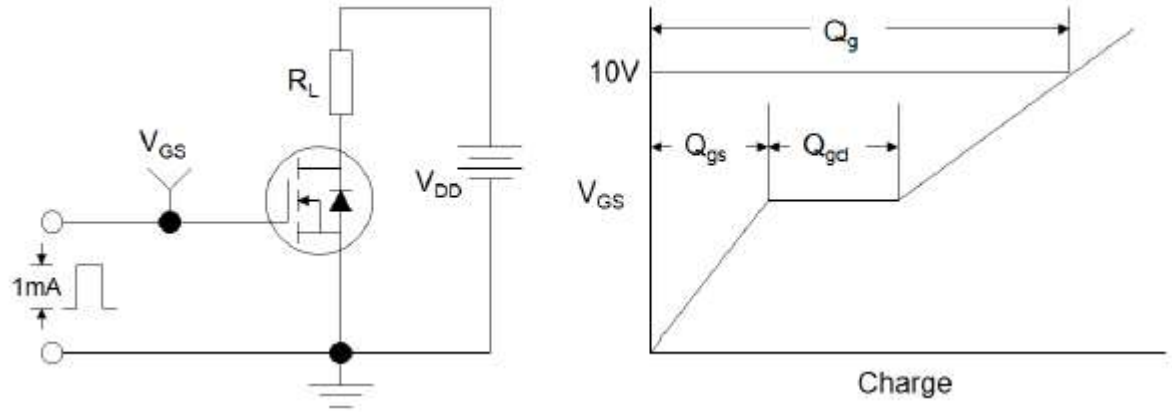


Figure1:Gate Charge Test Circuit & Waveform

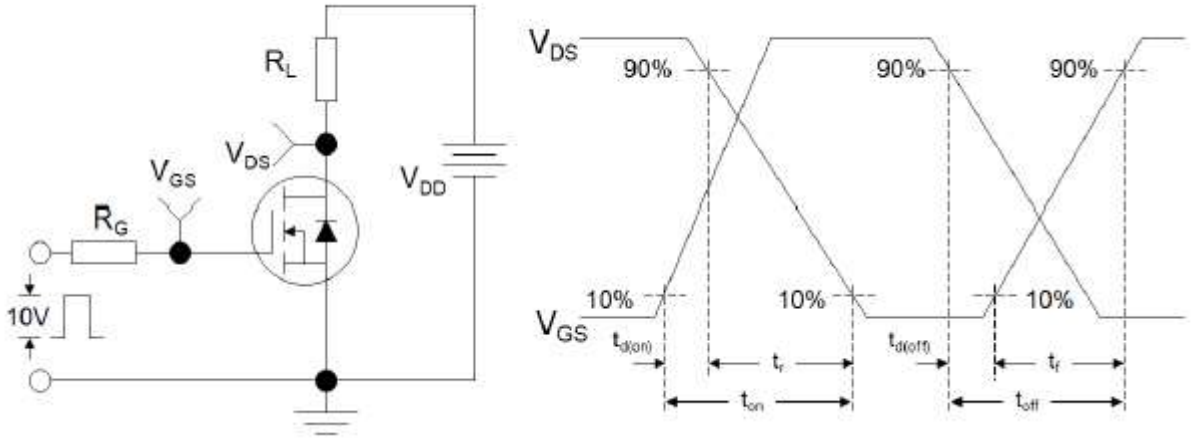


Figure 2: Resistive Switching Test Circuit & Waveforms

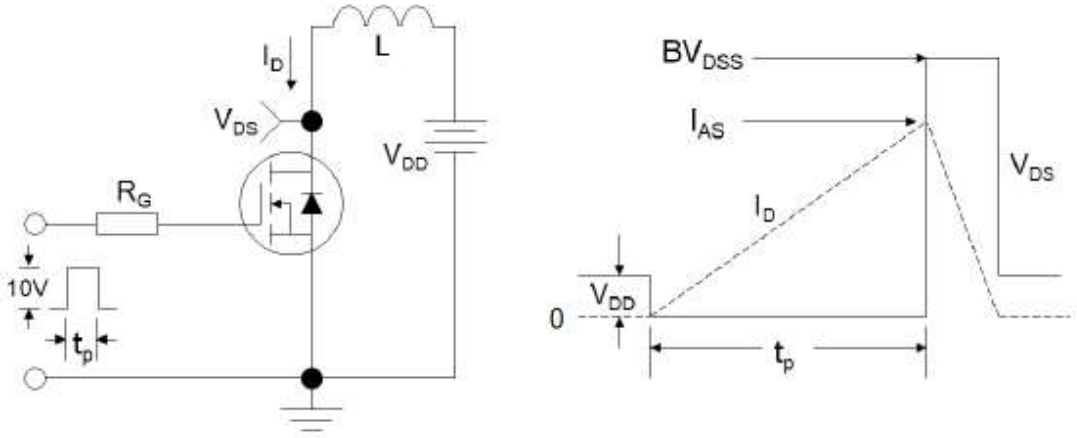
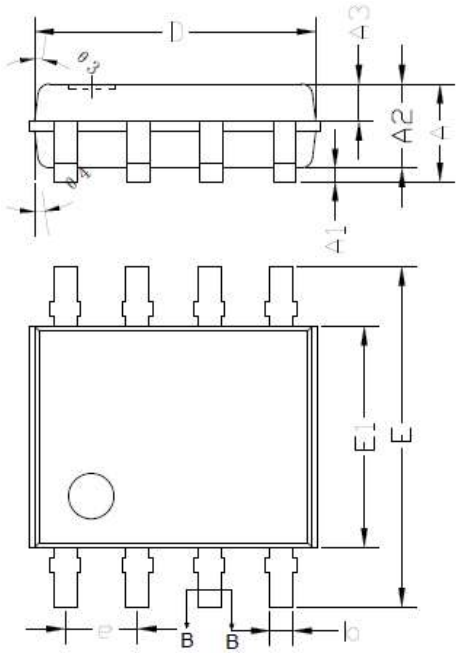
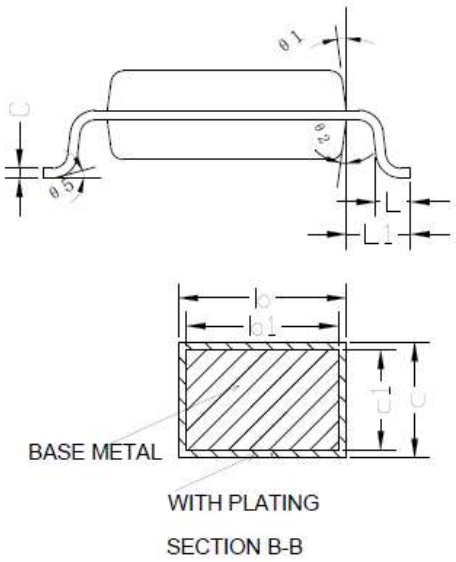


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

Package Mechanical Data-SOP-8





SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	--	--	1.65
A1	0.10	--	0.25
A2	1.40	1.42	1.50
A3	0.60	0.65	0.70
b	0.33	--	0.47
b1	0.32	0.41	0.44
c	0.20	--	0.24
c1	0.19	0.20	0.21
D	4.80	4.90	5.00
E	5.90	6.00	6.20
E1	3.85	3.90	4.00
e	1.27(BSC)		
L	0.50	0.60	0.70
L1	1.05(BSC)		
θ1	6°	~	12°
θ2	6°	~	12°
θ3	5°	~	10°
θ4	5°	~	10°
θ5	0°	~	6°