



Description

JMT Dual N-channel Enhancement Mode Power MOSFET

Features

- 30V, 4.5A
- $R_{DS(ON)} < 36\text{m}\Omega$ @ $V_{GS} = 10\text{V}$
- $R_{DS(ON)} < 56\text{m}\Omega$ @ $V_{GS} = 4.5\text{V}$
- Advanced Trench Technology
- Provide Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

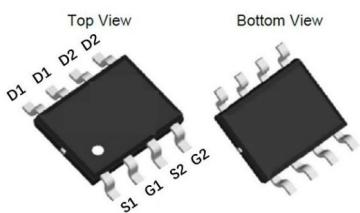
Application

- Load Switch
- PWM Application
- Power management

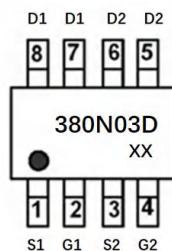


100% UIS TESTED!

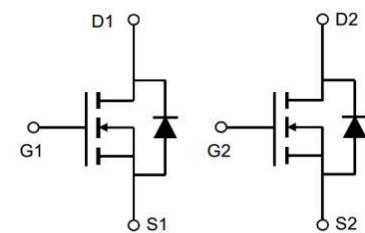
100% ΔV_{ds} TESTED!



SOP-8(Dual)



Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
380N03D	JMTP380N03D	TAPING	SOP-8	13inch	4000	48000

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_A = 25^\circ\text{C}$	4.5	A
		$T_A = 100^\circ\text{C}$	2.9	A
I_{DM}	Pulsed Drain Current ^{note1}		18	A
EAS	Single Pulsed Avalanche Energy ^{note2}		3	mJ
P_D	Power Dissipation	$T_A = 25^\circ\text{C}$	1.2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		104	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}= \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$R_{\text{DS}(\text{on})}$ Note3	Static Drain-Source on-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=3\text{A}$	-	28	36	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=2\text{A}$	-	40	56	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$	-	233	-	pF
C_{oss}	Output Capacitance		-	44	-	pF
C_{rss}	Reverse Transfer Capacitance		-	33	-	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=15\text{V}$, $I_D=2\text{A}$, $V_{\text{GS}}=10\text{V}$	-	3	-	nC
Q_{gs}	Gate-Source Charge		-	0.5	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	0.8	-	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=15\text{V}$, $I_D=3\text{A}$, $R_{\text{GEN}}=3\Omega$, $V_{\text{GS}}=10\text{V}$	-	4	-	ns
t_r	Turn-on Rise Time		-	2.1	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	15	-	ns
t_f	Turn-off Fall Time		-	3.2	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_{S}	Maximum Continuous Drain to Source Diode Forward Current	-	-	4.5	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	18	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{S}}=4.5\text{A}$	-	-	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{\text{DD}}=15\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{\text{AS}}=3.5\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

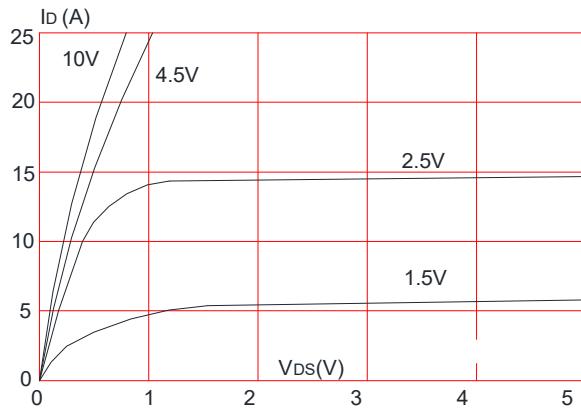


Figure 2: Typical Transfer Characteristics

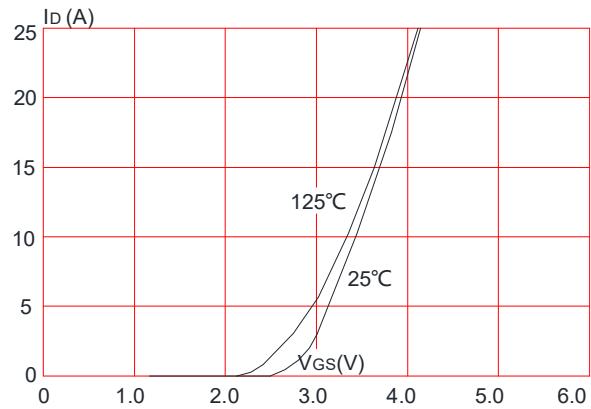


Figure 3: On-resistance vs. Drain Current

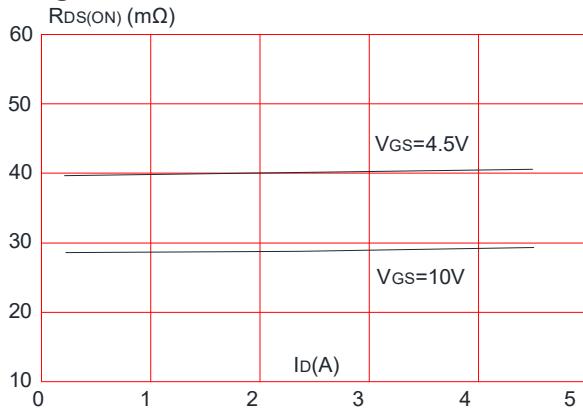


Figure 4: Body Diode Characteristics

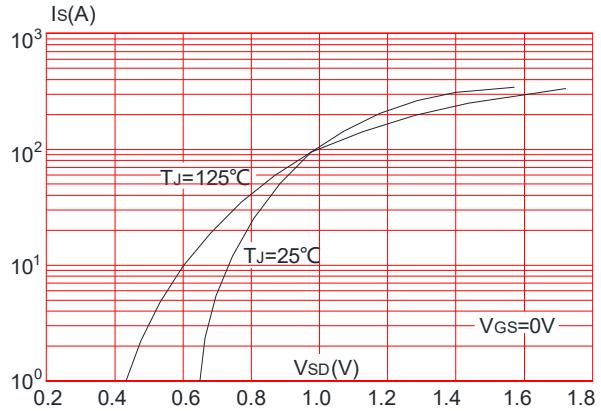


Figure 5: Gate Charge Characteristics

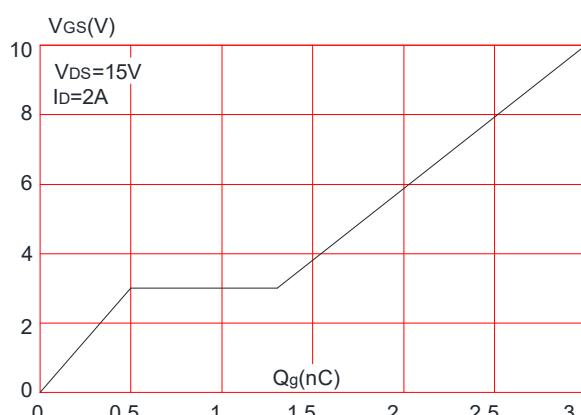


Figure 6: Capacitance Characteristics

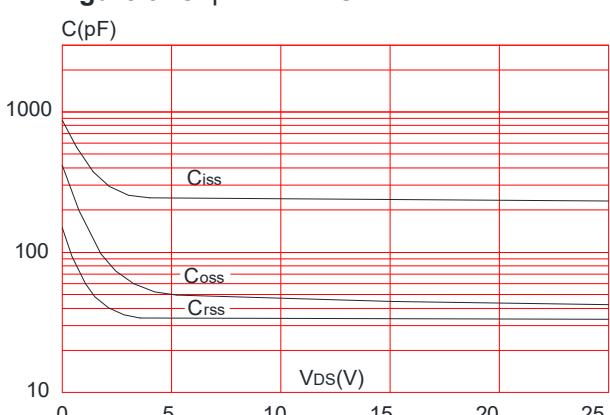


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

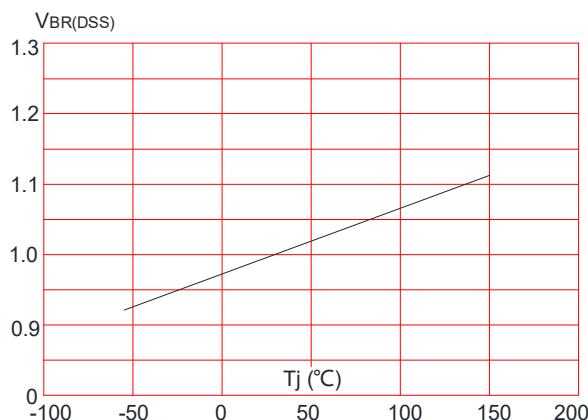


Figure 9: Maximum Safe Operating Area

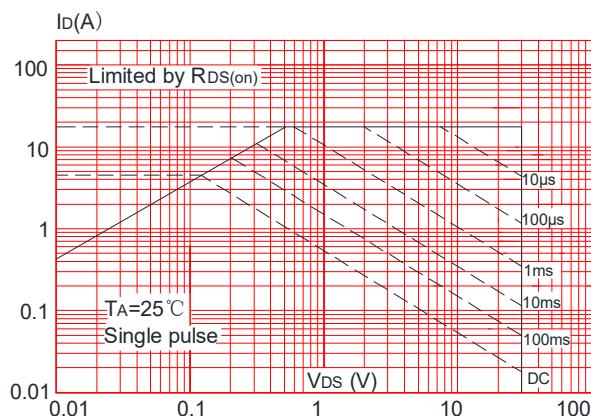


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

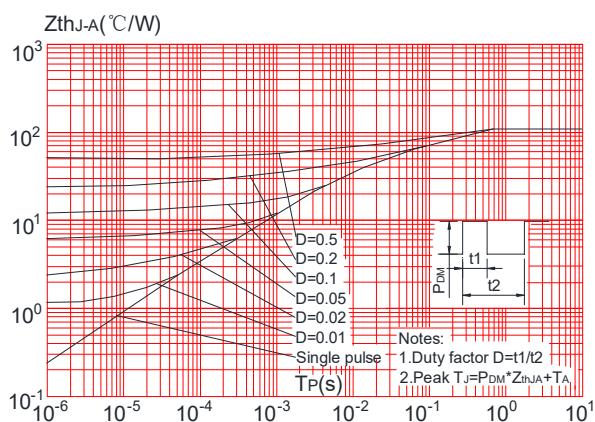


Figure 8: Normalized on Resistance vs. Junction Temperature

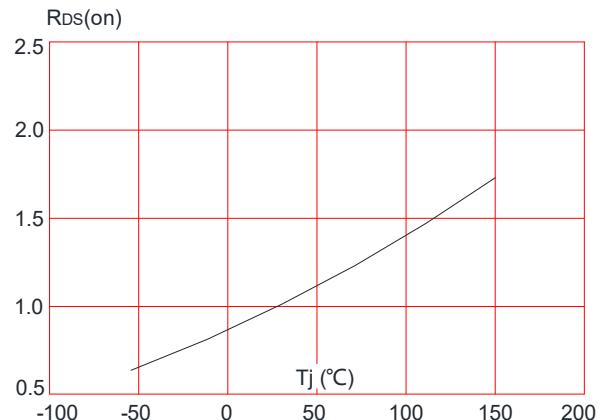
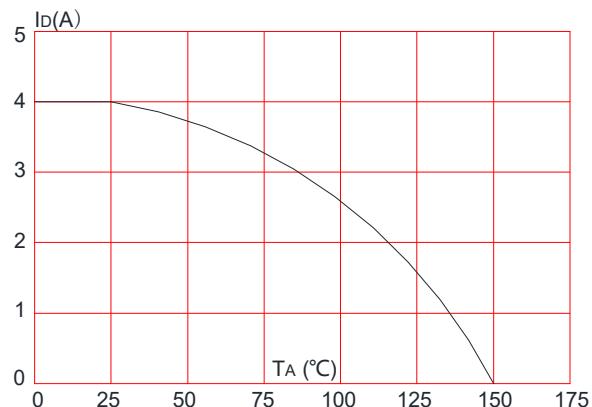


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



Test Circuit

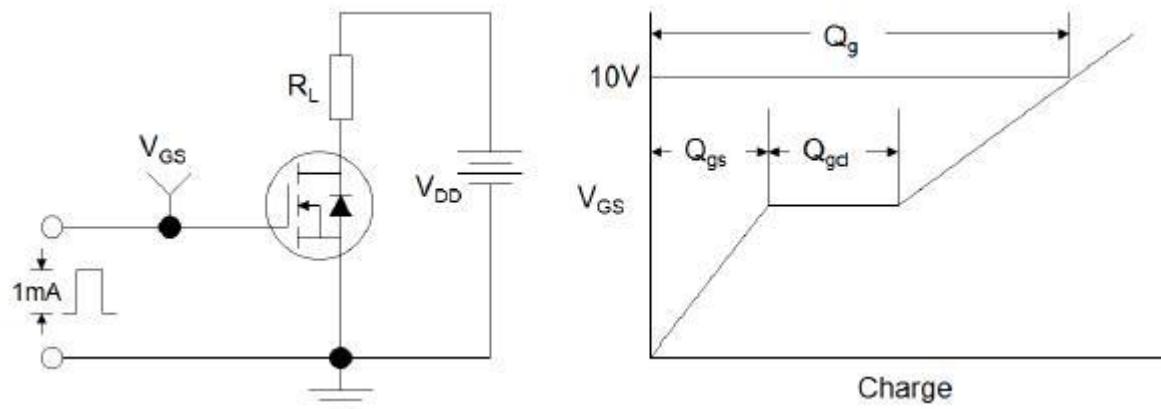


Figure1:Gate Charge Test Circuit & Waveform

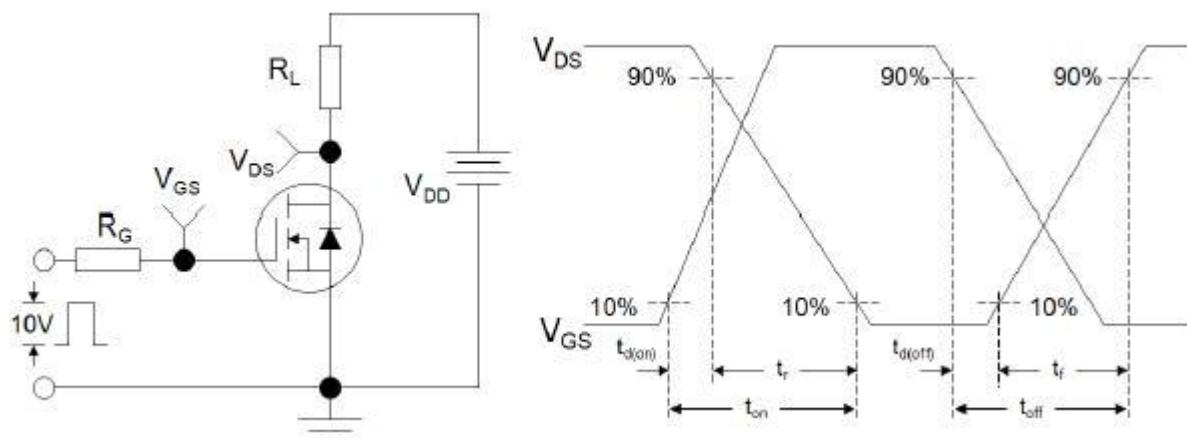


Figure 2: Resistive Switching Test Circuit & Waveforms

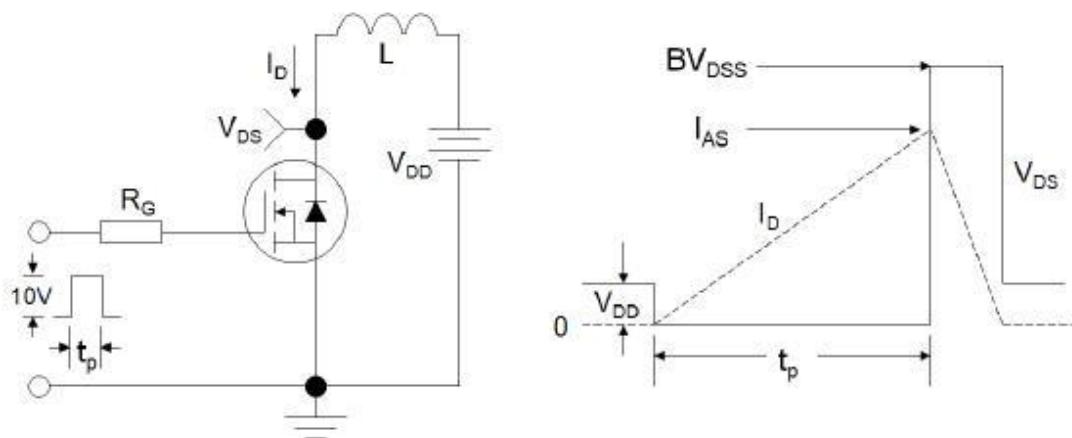
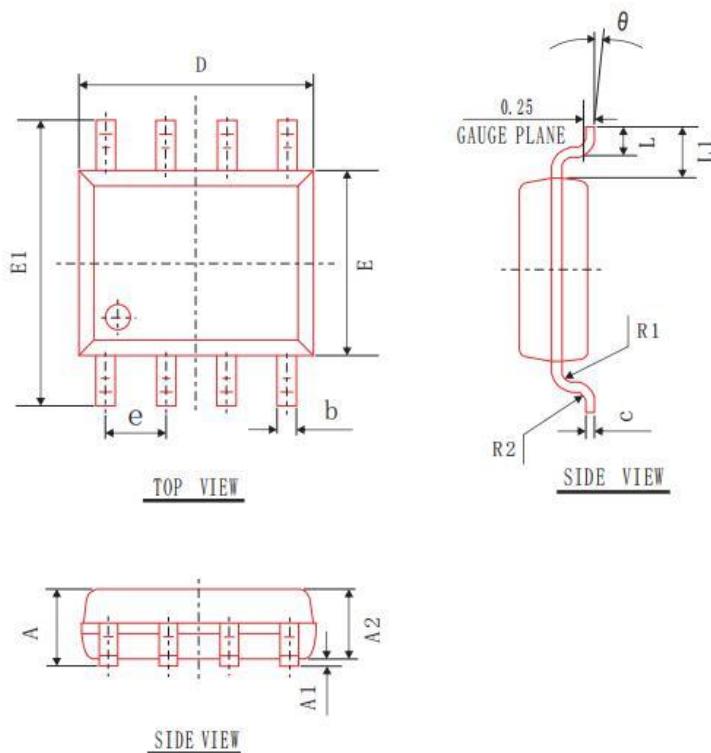


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



Package Mechanical Data-SOP-8

COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	1.40	1.60	1.80
A1	0.05	0.15	0.25
A2	1.35	1.45	1.55
b	0.30	0.40	0.50
c	0.153	0.203	0.253
D	4.80	4.90	5.00
E	3.80	3.90	4.00
E1	5.80	6.00	6.20
L	0.45	0.70	1.00
θ	2°	4°	6°
L1	1.04 REF		
e	1.27 BSC		
R1	0.07 TYP		
R2	0.07 TYP		

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