



## Description

### JMT Dual N-channel Enhancement Mode Power MOSFET

#### Features

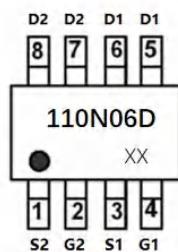
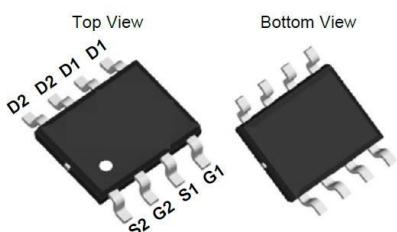
- 60V,11A  
 $R_{DS(ON)} < 14m\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(ON)} < 18m\Omega$  @  $V_{GS} = 4.5V$
- Advanced Trench Technology
- Provide Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

- Load Switch
- PWM Application
- Power management

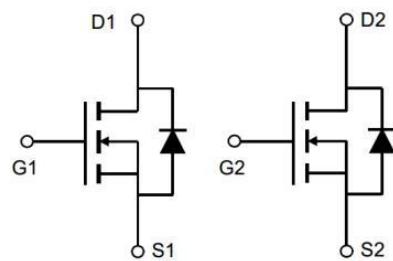


100% UIS TESTED!  
100%  $\Delta V_{ds}$  TESTED!



SOP-8(Dual)

Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
110N06D	JMTP110N06D	TAPING	SOP-8	13inch	4000	48000

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		60	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ C$	11	A
		$T_A = 100^\circ C$	7.2	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		44	A
EAS	Single Pulsed Avalanche Energy <sup>note2</sup>		81	mJ
$P_D$	Power Dissipation	$T_A = 25^\circ C$	3.1	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		40	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

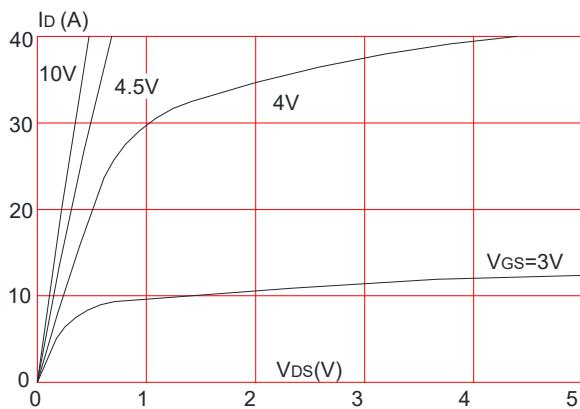
Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	60	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}= \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	1.0	-	2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$ , $I_D=11\text{A}$	-	11	14	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=8\text{A}$	-	13	18	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	4605	-	pF
$C_{oss}$	Output Capacitance		-	215	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	191	-	pF
$Q_g$	Total Gate Charge	$V_{DS}=30\text{V}$ , $I_D=6\text{A}$ , $V_{GS}=10\text{V}$	-	77	-	nC
$Q_{gs}$	Gate-Source Charge		-	9	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	23	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=30\text{V}$ , $I_D=11\text{A}$ , $R_G=1.8\Omega$ , $V_{GS}=10\text{V}$	-	7.1	-	ns
$t_r$	Turn-on Rise Time		-	5.3	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	27.2	-	ns
$t_f$	Turn-off Fall Time		-	6.2	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current	-	-	11	-	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	44	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=11\text{A}$	-	-	1.2	V
$trr$	Body Diode Reverse Recovery Time	$I_F=11\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	29	-	ns
$Qrr$	Body Diode Reverse Recovery Charge		-	45	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

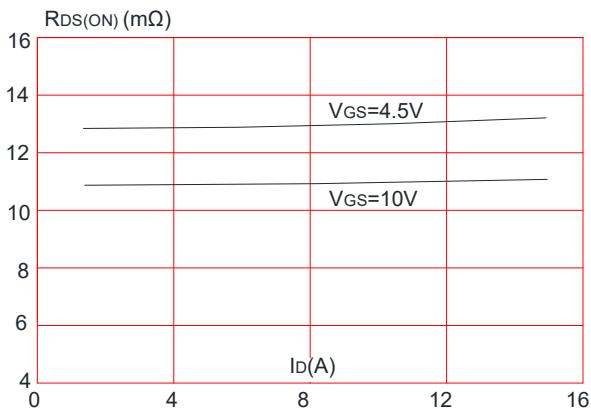
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=30\text{V}$ ,  $V_G=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$ ,  $I_{AS}=18\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

## Typical Performance Characteristics

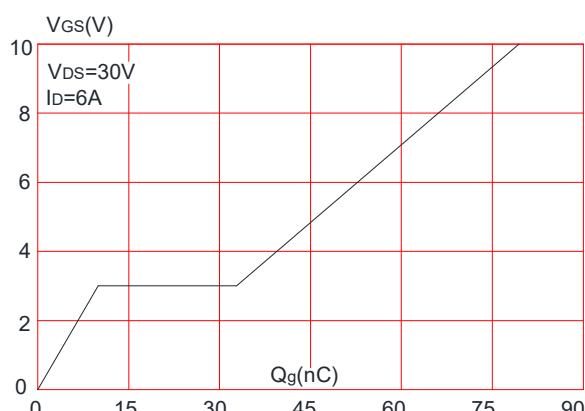
**Figure 1:** Output Characteristics



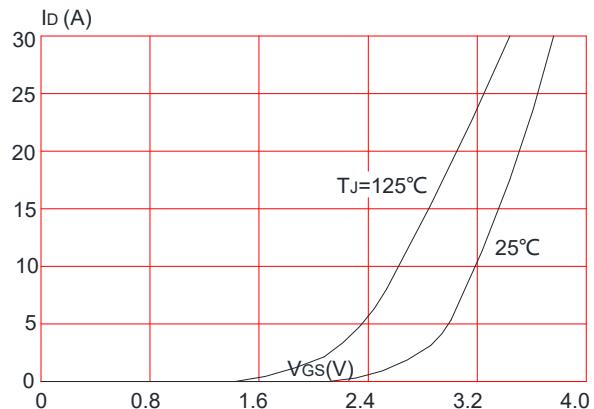
**Figure 3:** On-resistance vs. Drain Current



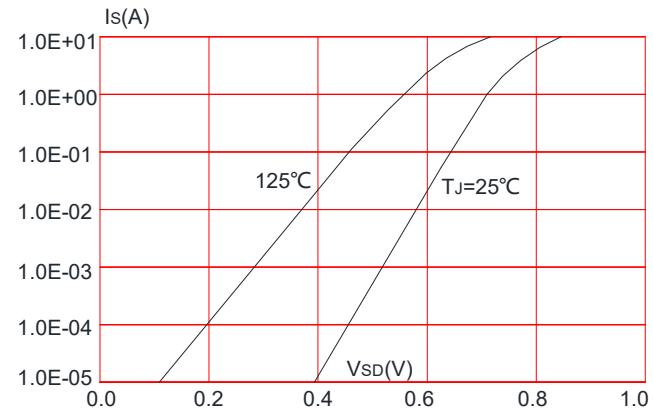
**Figure 5:** Gate Charge Characteristics



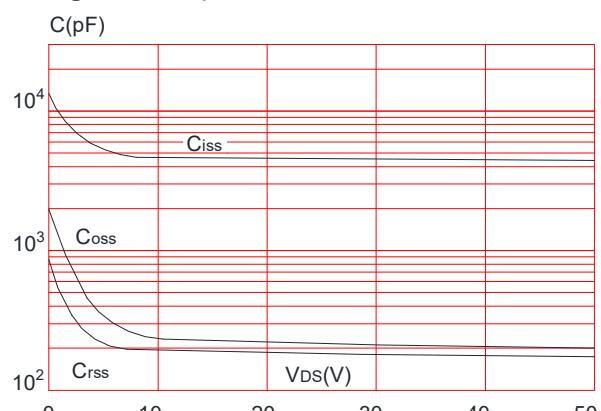
**Figure 2:** Typical Transfer Characteristics



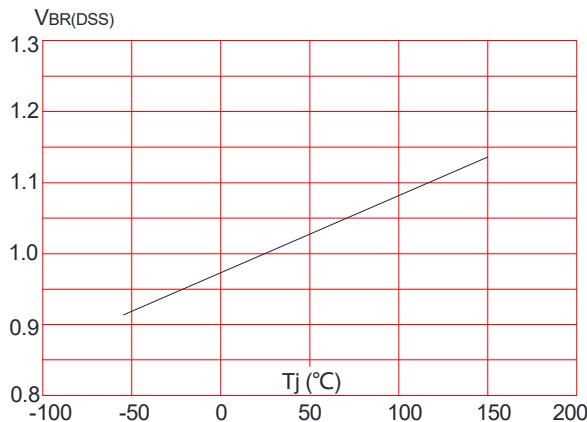
**Figure 4:** Body Diode Characteristics



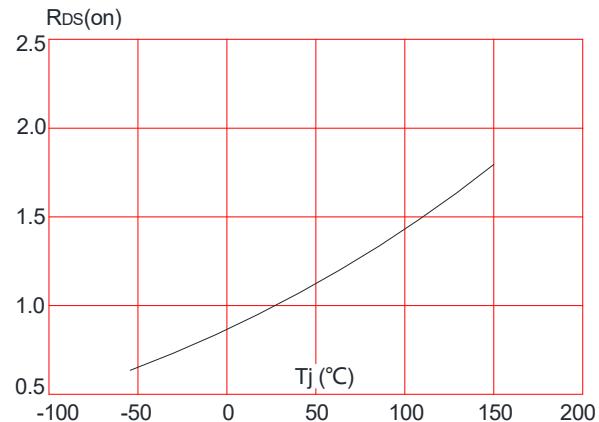
**Figure 6:** Capacitance Characteristics



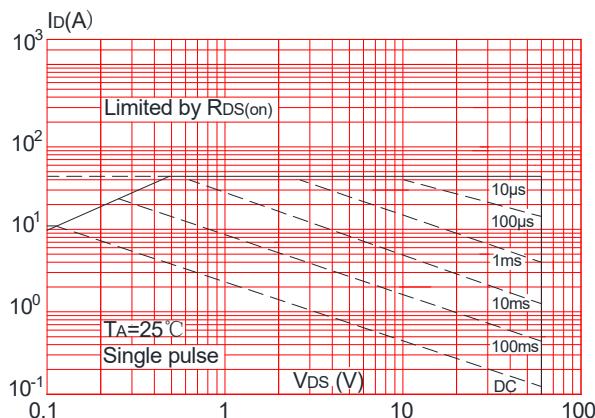
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



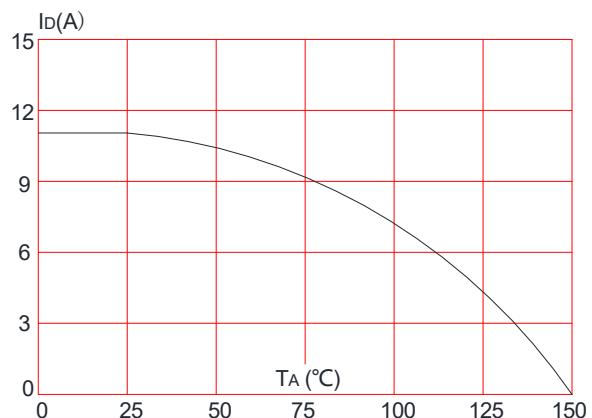
**Figure 8:** Normalized on Resistance vs. Junction Temperature



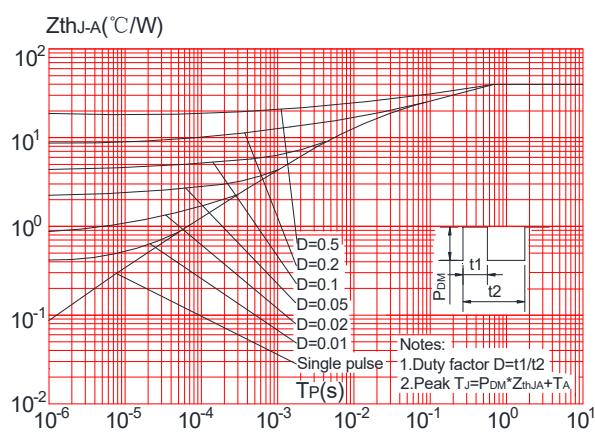
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



## Test Circuit

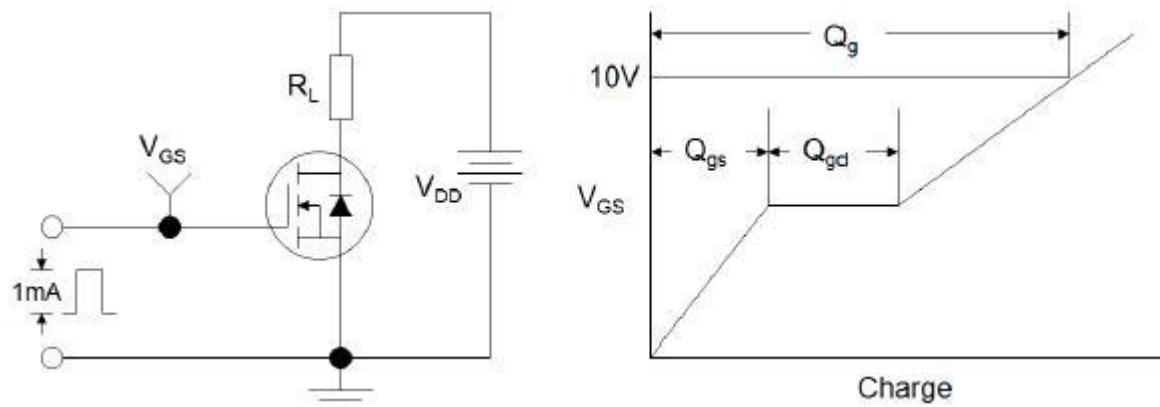


Figure1:Gate Charge Test Circuit & Waveform

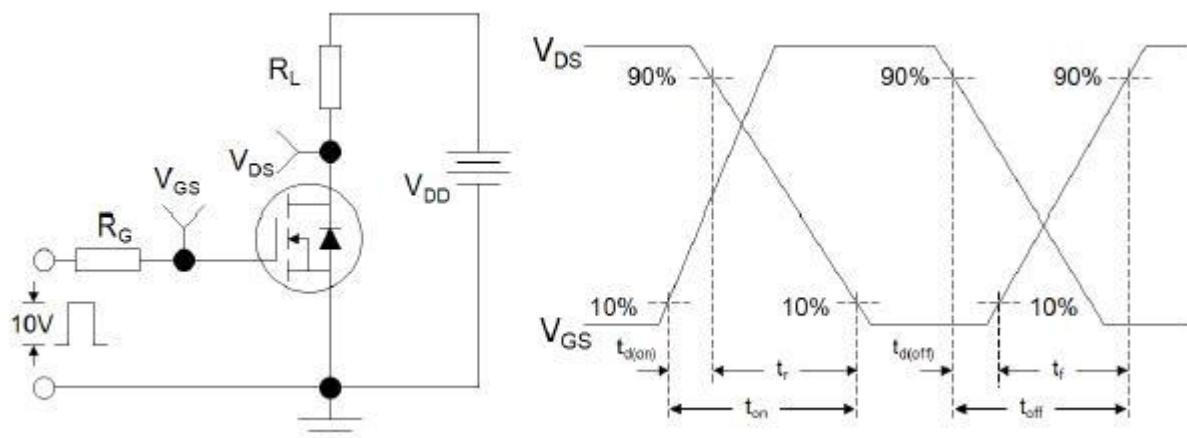


Figure 2: Resistive Switching Test Circuit & Waveforms

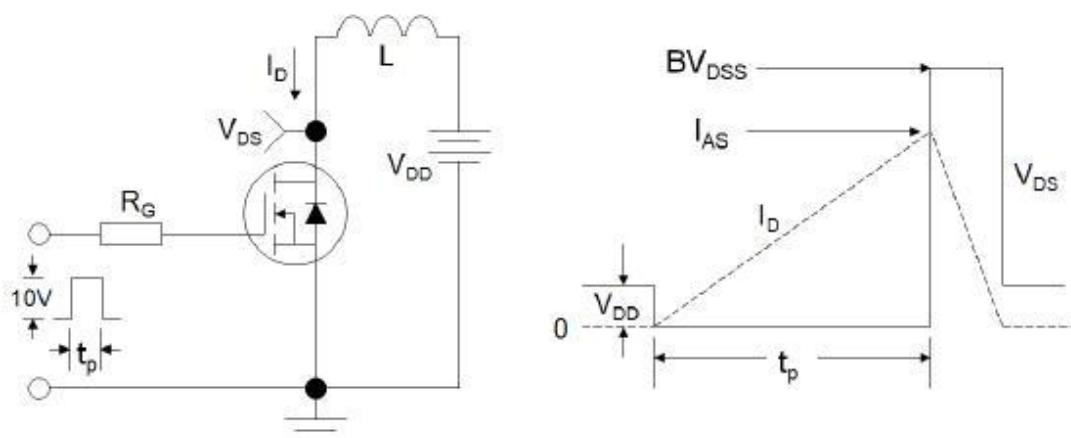
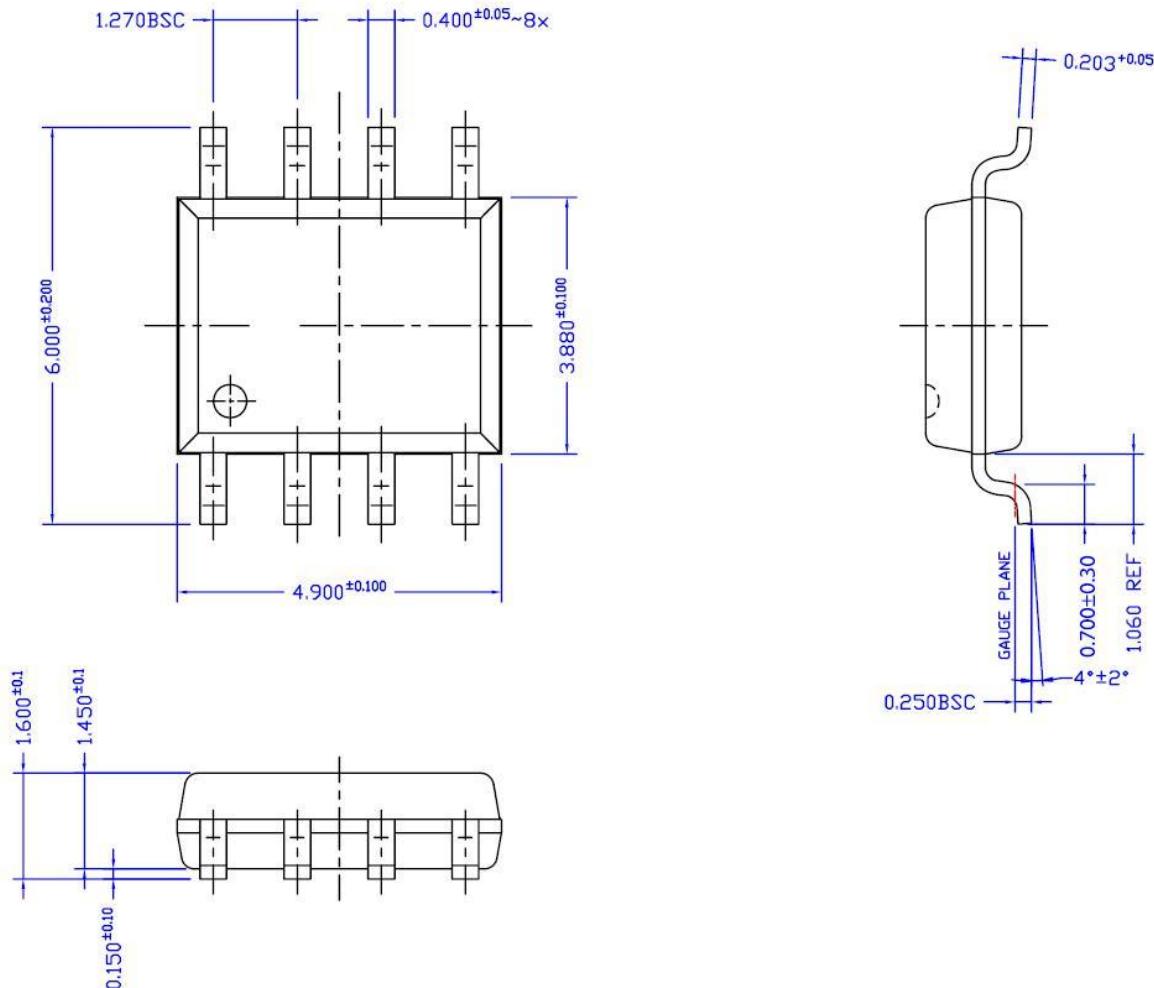


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## Package Mechanical Data- SOP-8



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